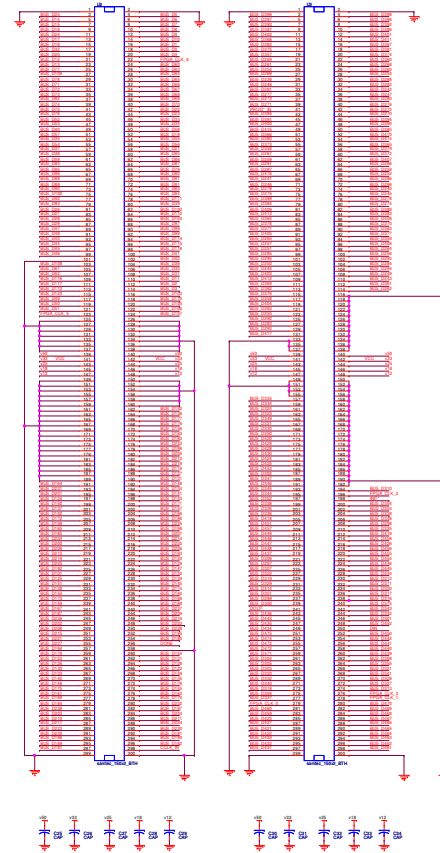
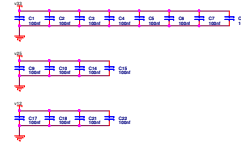


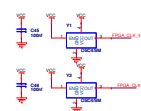
U1

xc3s1500fg676

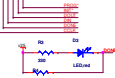
FPGA bypass caps



Optional local oscillators



Unconnected mode
pins default to
tri-state
configuration



Series resistors
limit current when
using 3.3V
configuration.