

090122 HjC

FMS QT bits to DSM:

Bits	Description
0-4	Daughter A sum
5-9	Daughter B sum
10-14	Daughter C sum
15-19	Daughter D sum
20-26	HT ADC
27-31	HT ID

HT ADC uses bits 5-11 (starting from 0)

Daughter sums: First form 15 bits sums. If any bit 10-14 is "on" then returns "11111", ie, overflow. If not, then returns bits 5-9 of the sum.