090126 HjC

TAC Timing window: The yellow shows the QT gate. The 30 mv “PMT” signal is in pink – because of 50 ohm splitter this was really 15 mv going into QT board. The TAC signal going into the QT is blue. Note the 45ns delay between the leading edge of the signal and when the TAC current pulse begins. In the second picture the TAC current is virtually 0. This shows that the effective window for the QT for a single channel is at least 30 ns wide.

20090126

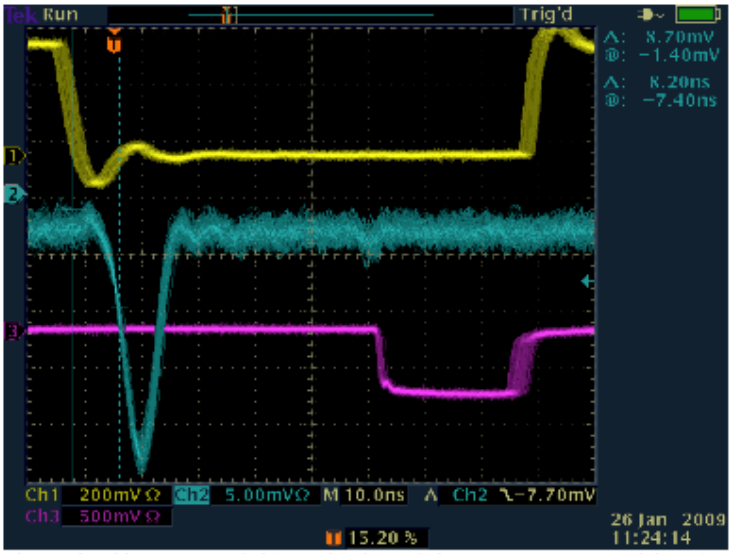

Fig. 1 ch1=bbq:0x10 tp1 / ch2=analog input / ch3=TAC output; WF delay=65ns

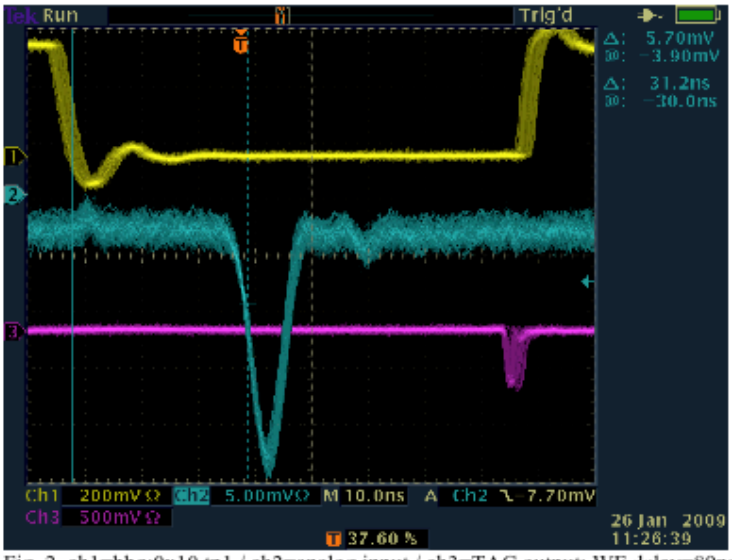

Fig. 2 ch1=bbq:0x10 tp1 / ch2=analog input / ch3=TAC output; WF delay=89ns

data taken by L.Bland.

TAC thresholds: register 0xYY80410c = 0x3ff gives 432mv threshold (090125)