component side

s1

s1 - selects the board base address. The MSB is the pins on the right hand side. A jumper across the pins forms a 1. No jumper is a 0.

J2 J1 - selects which bit in the bitmask this TCD responds to (J1) and which busy bit this TCD drives (J2).

No other jumpers should be changed without consulting an expert.

J2   J1

STAR Clock/Trigger Distribution Board - TCD2002A

front panel

SIZE A	REV	DRAWING #	

DWN BY	Eleanor Judd	DATE 12/19/03	SCALE	UC Berkeley - Space Sciences Laboratory Crawford Group
			DO NOT SCALE DRAWING	
COG. ENG.	Fred Bieser	DATE	UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE INCHES DECIMAL TOLERANCES	TITLE TCD Hardware switch settings
ALL SCREW THREADS PER ANSI 14.6 BREAK EDGES 0.020 MAX ON MACHINE WORK. REMOVE BURRS, LOOSE SCALE AND WELD SPLATTER REFERENCE - ANSI 14.5 & B46.1			.X = ±0.06 .XX = ±0.02 .XXX = ±0.004 ANGULAR TOLERANCE ± 1 DEG.	