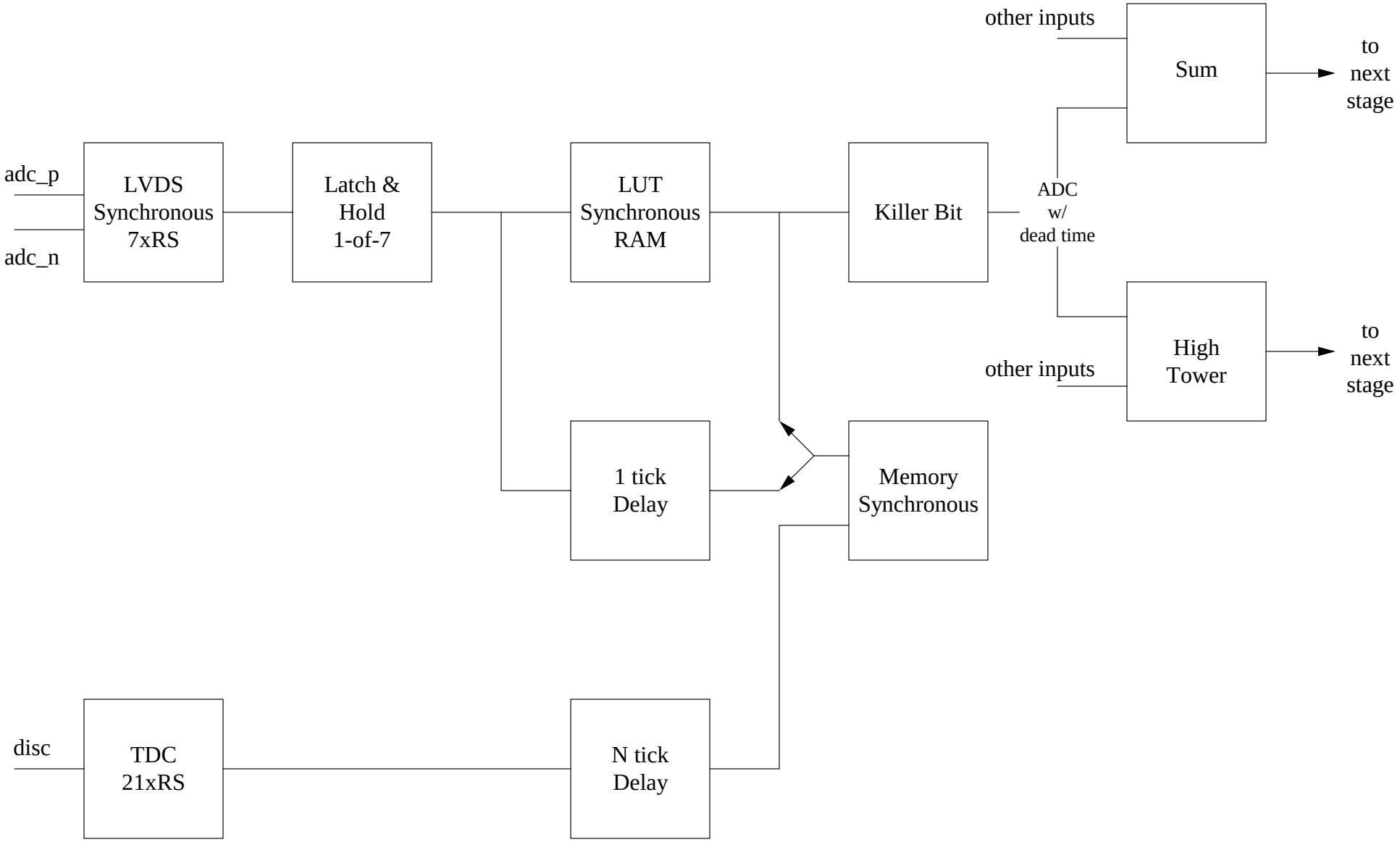
other inputs
Sum
to next stage
adc_p
adc_n
LVDS Synchronous 7xRS
Latch & Hold 1-of-7
LUT Synchronous RAM
Killer Bit
ADC w/ dead time
High Tower
other inputs
to next stage
1 tick Delay
Memory Synchronous
disc
TDC 21xRS
N tick Delay