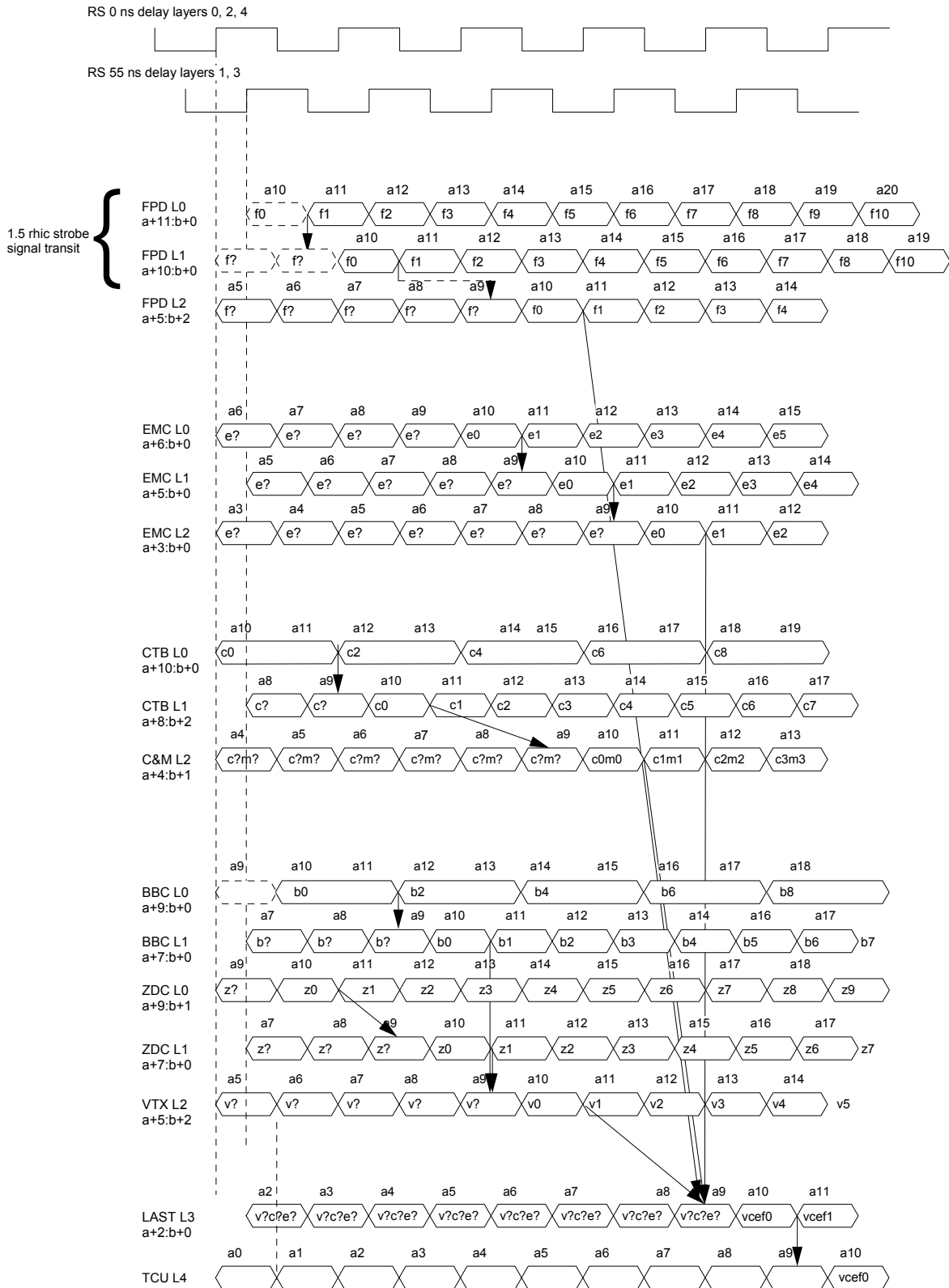


# TRIGGER timing 2004



this shows the timing of the data in the DSM tree for the various layers with the phase adjustment between the layers set to be 55ns in the RCC. The data in this diagram is shown at the output of the DSM. The DSM clocks data in and out on the rising edge of its phase of the RHIC strobe. The data actually takes ~25ns to go from DSM(out)->DSMI->cable->DSMI->DSM(in). This diagram is used to set the correct phase adjustments, address offsets and blanks.