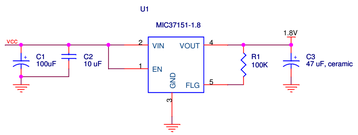
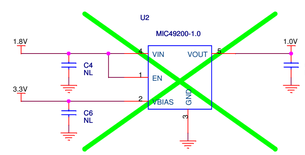
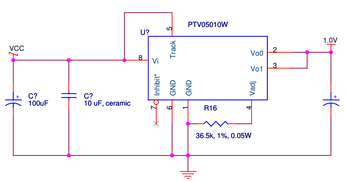
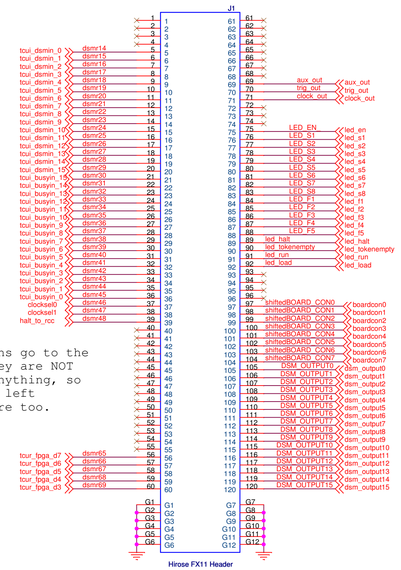


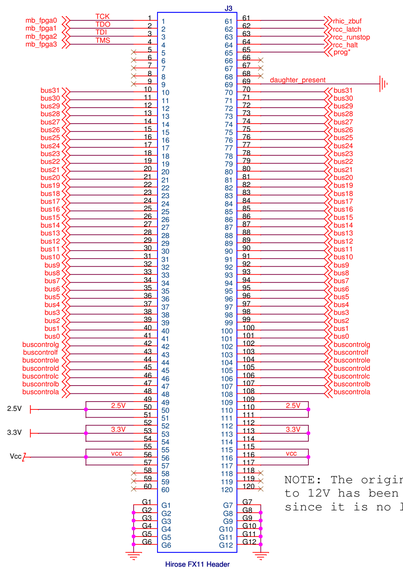




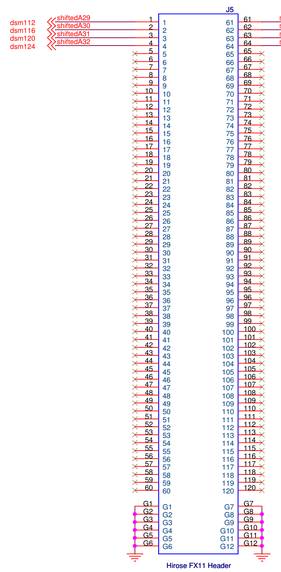
NOTE: The original 1.0V regulator could not provide enough current so it was replaced with the PTV05010W from Texas Instruments. All of parts U2, C4, C5 and C6 are NO LOAD The replacement regulator connectors to pads 3 (GND) and 5 (1.0V) of the old U2 footprint. The remianing U2 pads have no connection For the PTV05010W to work properly the 36.5k resistor MUST be connected directly between pin 4 (Vadj) and pin 1 (GND).



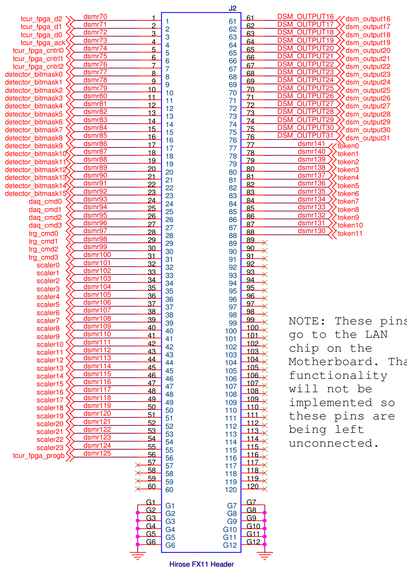
NOTE: These pins go to the TCUR, where they are NOT CONNECTED to anything, so they have been left unconnected here too.



NOTE: The original connection to 12V has been removed, since it is no longer needed.

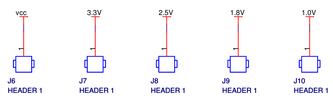


NOTE: These pins go to the RAM chip on the Motherboard. That functionality will not be implemented so these pins are being left unconnected.

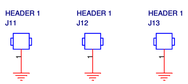


NOTE: These pins go to the LAN chip on the Motherboard. That functionality will not be implemented so these pins are being left unconnected.

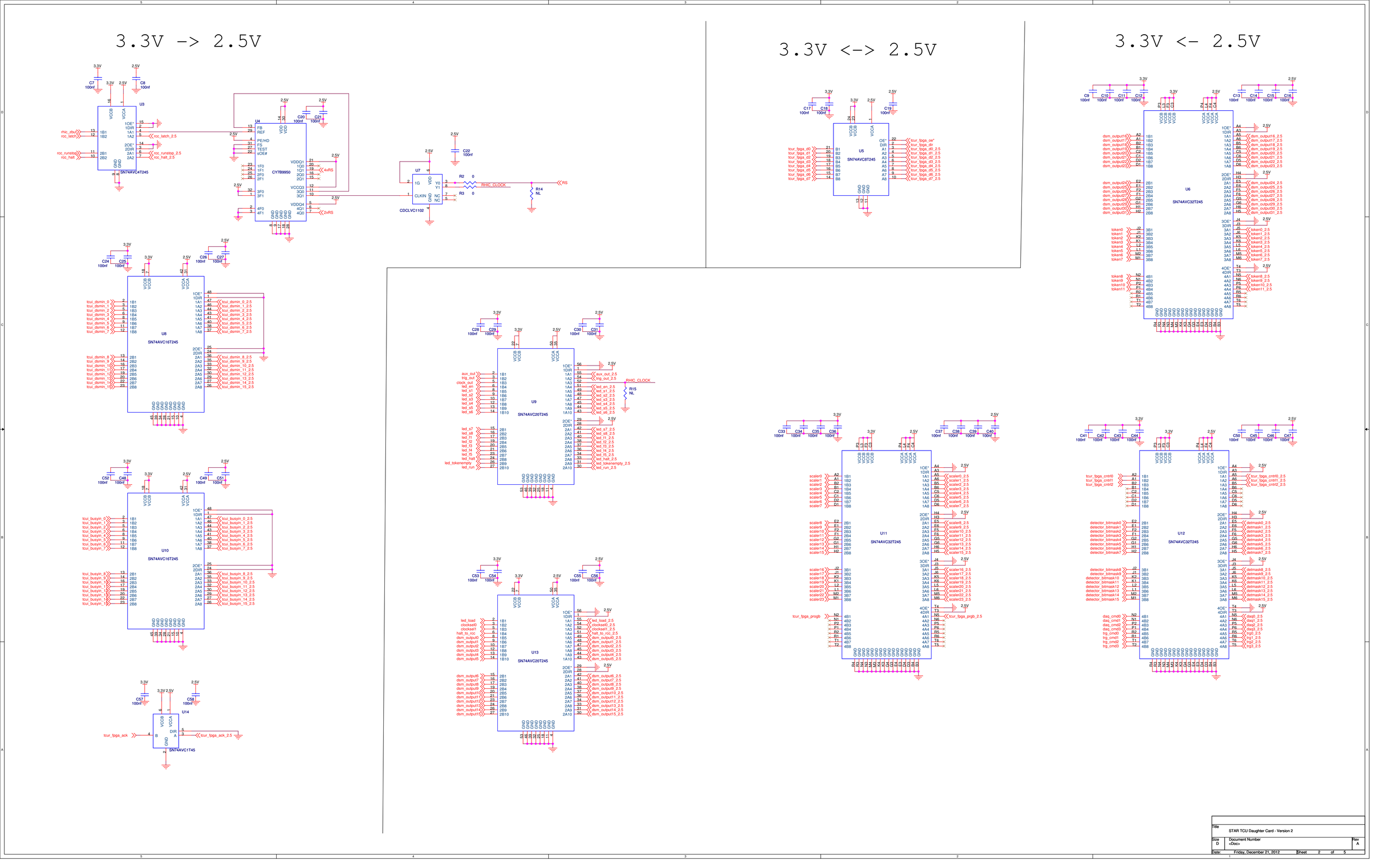
Test Points for Power
DO NOT LOAD



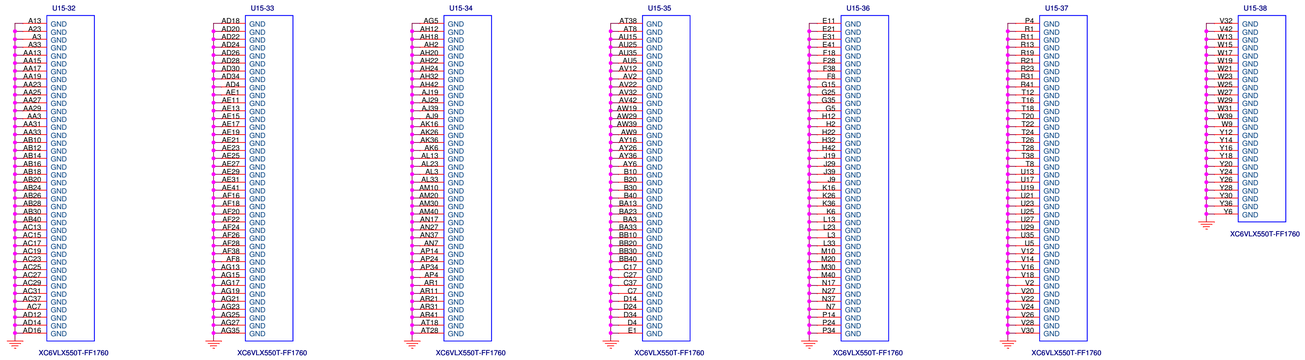
Test Points for GND
DO NOT LOAD

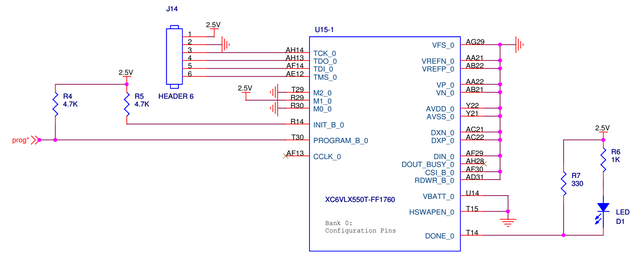


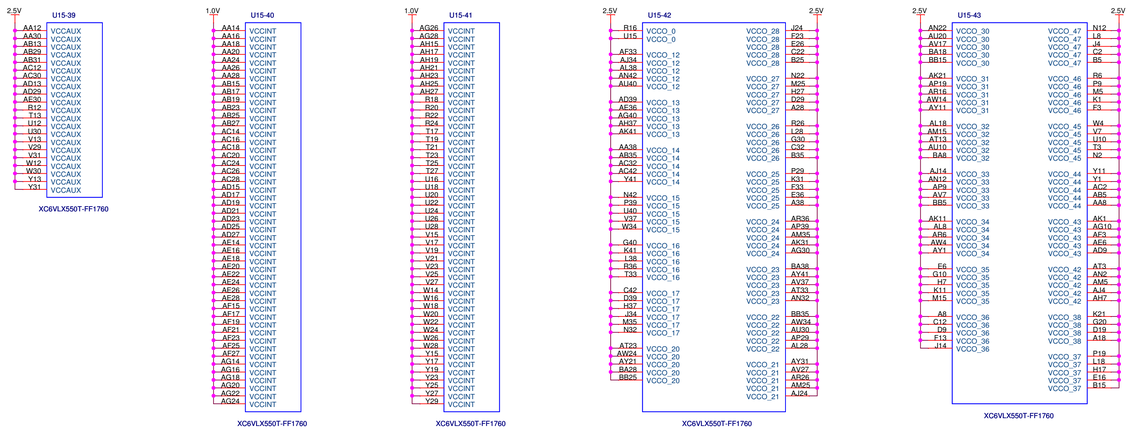
Title	STAR TCU Daughter Card - Version 2
Size D	Document Number <Doc>
Rev A	
Date: Wednesday, May 29, 2013	Sheet 1 of 5

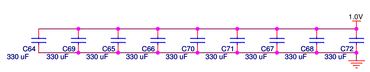


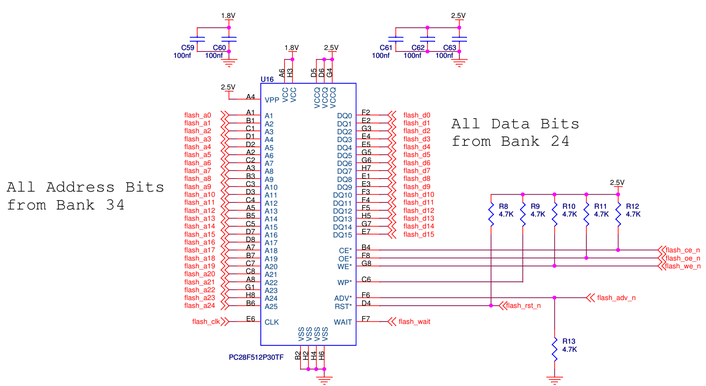
Title	STAR TCU Daughter Card - Version 2	
Size D	Document Number <Doc>	Rev A
Date: Friday, December 21, 2012	Sheet 2 of 5	



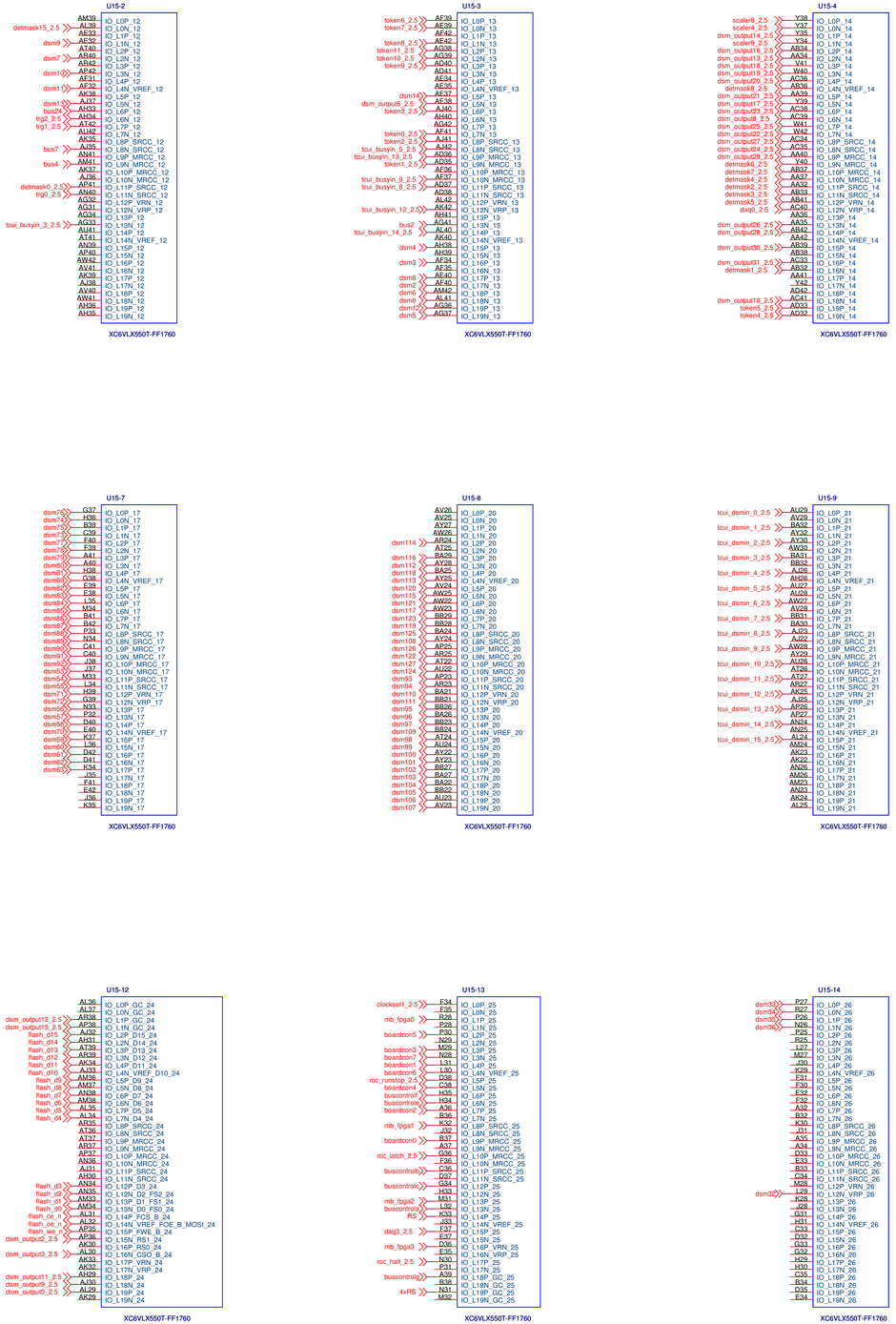








Title	STAR TCU Daughter Card - Version 2
Size D	Document Number <Doc>
Rev A	
Date: Monday, December 17, 2012	Sheet 3 of 5



Title	STAR TCU Daughter Card - Version 2	
Size D	Document Number <Doc>	Rev A
Date: Monday, December 17, 2012	Sheet 4 of 5	

U15-17

halt_to_rcc_2.5	AU18	IO_L0P_30
	AV18	IO_L0N_30
	AT19	IO_L1P_30
	AR19	IO_L1N_30
	BB14	IO_L2P_30
	BA14	IO_L2N_30
	BA17	IO_L3P_30
	BA16	IO_L3N_30
	BB16	IO_L4P_30
	BA15	IO_L4N_VREF_30
	AU19	IO_L5P_30
	AV19	IO_L5N_30
	AV20	IO_L6P_30
	AW20	IO_L6N_30
	BB17	IO_L7P_30
	BB18	IO_L7N_30
	AY17	IO_L8P_SRCC_30
	AW17	IO_L8N_SRCC_30
	BA20	IO_L9P_MRCC_30
	BB19	IO_L9N_MRCC_30
	AM22	IO_L10P_MRCC_30
	AL22	IO_L10N_MRCC_30
	AW21	IO_L11P_SRCC_30
	AY20	IO_L11N_SRCC_30
	AY19	IO_L12P_VRN_30
	AW18	IO_L12N_VRP_30
	AR22	IO_L13P_30
	AP22	IO_L13N_30
	AY18	IO_L14P_30
	BA19	IO_L14N_VREF_30
	AN21	IO_L15P_30
	AM21	IO_L15N_30
	AN20	IO_L16P_30
	AP20	IO_L16N_30
	AT21	IO_L17P_30
	AT20	IO_L17N_30
	AP21	IO_L18P_30
	AR20	IO_L18N_30
	AU21	IO_L19P_30
	AV21	IO_L19N_30

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U15-18

led_en_2.5	AV13	IO_L0P_31
	AV14	IO_L0N_31
led_s1_2.5	AP18	IO_L1P_31
	AN18	IO_L1N_31
led_s2_2.5	AW11	IO_L2P_31
	AY10	IO_L2N_31
led_s3_2.5	AN19	IO_L3P_31
	AM19	IO_L3N_31
led_s4_2.5	BA11	IO_L4P_31
	BA10	IO_L4N_VREF_31
led_s5_2.5	AU16	IO_L5P_31
	AV15	IO_L5N_31
led_s6_2.5	AJ20	IO_L6P_31
	AK19	IO_L6N_31
led_s7_2.5	AP17	IO_L7P_31
	AR17	IO_L7N_31
led_s8_2.5	BA12	IO_L8P_SRCC_31
	BB11	IO_L8N_SRCC_31
led_f1_2.5	AT17	IO_L9P_MRCC_31
	AR18	IO_L9N_MRCC_31
led_f2_2.5	AK20	IO_L10P_MRCC_31
	AJ21	IO_L10N_MRCC_31
led_f3_2.5	AV12	IO_L11P_SRCC_31
	AW13	IO_L11N_SRCC_31
led_f4_2.5	AW12	IO_L12P_VRN_31
	AV11	IO_L12N_VRP_31
led_f5_2.5	AY14	IO_L13P_31
	AT16	IO_L13N_31
	AT15	IO_L14P_31
	AW16	IO_L14N_VREF_31
	AW15	IO_L15P_31
	AL19	IO_L15N_31
	AM18	IO_L16P_31
	BB12	IO_L16N_31
	BB13	IO_L17P_31
	AL21	IO_L17N_31
	AL20	IO_L18P_31
	AU17	IO_L18N_31
	AV16	IO_L19P_31
		IO_L19N_31

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U15-19

tcur_fpga_d0_2.5	AU9	IO_L0P_32
	AV9	IO_L0N_32
	AP16	IO_L1P_32
	AN16	IO_L1N_32
tcur_fpga_d1_2.5	AL15	IO_L2P_32
	AK15	IO_L2N_32
	AR14	IO_L3P_32
	AR13	IO_L3N_32
tcur_fpga_d2_2.5	AT10	IO_L4P_32
	AT11	IO_L4N_VREF_32
	AU11	IO_L5P_32
	AV10	IO_L5N_32
tcur_fpga_d3_2.5	AU12	IO_L6P_32
	AV11	IO_L6N_32
	AK18	IO_L7P_32
	AJ18	IO_L7N_32
tcur_fpga_d4_2.5	AW8	IO_L8P_SRCC_32
	AV8	IO_L8N_SRCC_32
	AT14	IO_L9P_MRCC_32
	AR15	IO_L9N_MRCC_32
tcur_fpga_d5_2.5	AU14	IO_L10P_MRCC_32
	AU13	IO_L10N_MRCC_32
	AW10	IO_L11P_SRCC_32
	AV9	IO_L11N_SRCC_32
tcur_fpga_d6_2.5	AY8	IO_L12P_VRN_32
	BA7	IO_L12N_VRP_32
	BB9	IO_L13P_32
	BA9	IO_L13N_32
tcur_fpga_d7_2.5	AM16	IO_L14P_32
	AL16	IO_L14N_VREF_32
	AK17	IO_L15P_32
	AJ17	IO_L15N_32
	AT12	IO_L16P_32
	AR12	IO_L16N_32
	BB7	IO_L17P_32
	BB8	IO_L17N_32
	AP15	IO_L18P_32
	AN15	IO_L18N_32
	AM17	IO_L19P_32
	AL17	IO_L19N_32

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U15-20

	AT7	IO_L0P_33
	AR8	IO_L0N_33
dsm_output5_2.5	AN10	IO_L1P_33
dsm_output1_2.5	AM11	IO_L1N_33
flash_wait	AK13	IO_L2P_33
	AJ13	IO_L2N_33
	AR10	IO_L3P_33
	AP10	IO_L3N_33
	AP8	IO_L4P_33
	AN8	IO_L4N_VREF_33
dsm_output7_2.5	AK14	IO_L5P_33
	AJ15	IO_L5N_33
	AL16	IO_L6P_33
	AH16	IO_L6N_33
tcur_fpga_prgb_2.5	AP12	IO_L7P_33
	AT8	IO_L7N_33
	AR7	IO_L8P_SRCC_33
	AT9	IO_L8N_SRCC_33
tcur_fpga_cntrl0_2.5	AR9	IO_L9P_MRCC_33
	AV6	IO_L9N_MRCC_33
	AU6	IO_L10P_MRCC_33
tcur_fpga_cntrl1_2.5	AW7	IO_L10N_MRCC_33
	AY7	IO_L11P_SRCC_33
	BA5	IO_L11N_SRCC_33
	AY5	IO_L12P_VRN_33
tcur_fpga_cntrl2_2.5	BB6	IO_L12N_VRP_33
	BA6	IO_L13P_33
	AM12	IO_L13N_33
	AN11	IO_L14P_33
	AW6	IO_L14N_VREF_33
tcur_fpga_oe	AW5	IO_L15P_33
	AM13	IO_L15N_33
	AN13	IO_L16P_33
tcur_fpga_dir	AN14	IO_L16N_33
	AP13	IO_L17P_33
	AM14	IO_L17N_33
	AL14	IO_L18P_33
	AU8	IO_L18N_33
tcur_fpga_ack_2.5	AU7	IO_L19P_33
		IO_L19N_33

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U15-21

	AH11	IO_L0P_GC_34
	AJ11	IO_L0N_GC_34
	AW2	IO_L1P_GC_34
	AV1	IO_L1N_GC_34
flash_a15	AL7	IO_L2P_A15_D31_34
flash_a14	AK8	IO_L2N_A14_D30_34
flash_a13	AY2	IO_L3P_A13_D29_34
flash_a12	BA1	IO_L3N_A12_D28_34
flash_a11	AU4	IO_L4P_A11_D27_34
flash_a10	AV3	IO_L4N_VREF_A10_D26_34
flash_a9	AN6	IO_L5P_A09_D25_34
flash_a8	AM7	IO_L5N_A08_D24_34
flash_a7	AR5	IO_L6P_A07_D23_34
flash_a6	AP5	IO_L6N_A06_D22_34
flash_a5	BA2	IO_L7P_A05_D21_34
flash_a4	BB2	IO_L7N_A04_D20_34
	BB4	IO_L8P_SRCC_34
	BB3	IO_L8N_SRCC_34
	AY3	IO_L9P_MRCC_34
	AW3	IO_L9N_MRCC_34
	AV5	IO_L10P_MRCC_34
	AV4	IO_L10N_MRCC_34
	AK9	IO_L11P_SRCC_34
	AL9	IO_L11N_SRCC_34
flash_a3	AY4	IO_L12P_A03_D19_34
flash_a2	BA4	IO_L12N_A02_D18_34
flash_a1	AK10	IO_L13P_A01_D17_34
flash_a0	AJ11	IO_L13N_A00_D16_34
	AT5	IO_L14P_A25_34
flash_a24	AT4	IO_L14N_VREF_A24_34
flash_a23	AP7	IO_L15P_A23_34
flash_a22	AP6	IO_L15N_A22_34
flash_a21	AK12	IO_L16P_A21_34
flash_a20	AJ12	IO_L16N_A20_34
flash_a19	AM9	IO_L17P_A19_34
flash_a18	AL10	IO_L17N_A18_34
flash_a17	AL12	IO_L18P_A17_34
flash_a16	AN5	IO_L18N_A16_34
	AM8	IO_L19P_VRN_34
		IO_L19N_VRP_34

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U15-22

	F10	IO_L0P_35
	F9	IO_L0N_35
	D8	IO_L1P_35
	D7	IO_L1N_35
	H9	IO_L2P_SM0P_35
	G9	IO_L2N_SM0N_35
	H11	IO_L3P_SM1P_35
	H10	IO_L3N_SM1N_35
	M14	IO_L4P_35
	L14	IO_L4N_VREF_35
	F7	IO_L5P_SM2P_35
	E7	IO_L5N_SM2N_35
	G6	IO_L6P_SM3P_35
	F6	IO_L6N_SM3N_35
	J10	IO_L7P_SM4P_35
	J11	IO_L7N_SM4N_35
	K10	IO_L8P_SRCC_35
	L11	IO_L8N_SRCC_35
	K13	IO_L9P_MRCC_35
	J12	IO_L9N_MRCC_35
	G7	IO_L10P_MRCC_35
	G8	IO_L10N_MRCC_35
	K12	IO_L11P_SRCC_35
	L12	IO_L11N_SRCC_35
	L10	IO_L12P_SM5P_35
	K9	IO_L12N_SM5N_35
	M16	IO_L13P_SM6P_35
	L15	IO_L13N_SM6N_35
	M13	IO_L14P_35
	M12	IO_L14N_VREF_35
	J8	IO_L15P_SM7P_35
	H8	IO_L15N_SM7N_35
	N14	IO_L16P_VRN_35
	N15	IO_L16N_VRP_35
	N16	IO_L17P_35
	P16	IO_L17N_35
2xRS	R15	IO_L18P_GC_35
	P15	IO_L18N_GC_35
	K8	IO_L19P_GC_35
	J7	IO_L19N_GC_35

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U15-23

	D10	IO_L0P_36
	D11	IO_L0N_36
	A11	IO_L1P_36
	A12	IO_L1N_36
	G11	IO_L2P_36
	G12	IO_L2N_36
	A9	IO_L3P_36
	A10	IO_L3N_36
	C8	IO_L4P_36
	C9	IO_L4N_VREF_36
	E12	IO_L5P_36
	D12	IO_L5N_36
	E8	IO_L6P_36
	D8	IO_L6N_36
	B12	IO_L7P_36
	B11	IO_L7N_36
	B7	IO_L8P_SRCC_36
	A7	IO_L8N_SRCC_36
	B8	IO_L9P_MRCC_36
	B9	IO_L9N_MRCC_36
	J15	IO_L10P_MRCC_36
	J16	IO_L10N_MRCC_36
	C11	IO_L11P_SRCC_36
	C10	IO_L11N_SRCC_36
	J13	IO_L12P_VRN_36
	K14	IO_L12N_VRP_36
	H14	IO_L13P_36
	G14	IO_L13N_36
	A5	IO_L14P_36
	A6	IO_L14N_VREF_36
	E9	IO_L15P_36
	E10	IO_L15N_36
	L16	IO_L16P_36
	K15	IO_L16N_36
	G13	IO_L17P_36
	H13	IO_L17N_36
	C6	IO_L18P_36
	B6	IO_L18N_36
	F11	IO_L19P_36
	E12	IO_L19N_36

XC6VLX550T-FF1760

U15-24

	D13	IO_L0P_37
	E13	IO_L0N_37
	A14	IO_L1P_37
	A15	IO_L1N_37
	J17	IO_L2P_37
	B13	IO_L2N_37
	B14	IO_L3P_37
	P20	IO_L3N_37
	N19	IO_L4P_37
	E14	IO_L4N_VREF_37
	E15	IO_L5P_37
	F15	IO_L5N_37
	F14	IO_L6P_37
	C16	IO_L6N_37
	D16	IO_L7P_37
	H15	IO_L7N_37
	H16	IO_L8P_SRCC_37
	P21	IO_L8N_SRCC_37
	N20	IO_L9P_MRCC_37
	G16	IO_L9N_MRCC_37
	F16	IO_L10P_MRCC_37
	C15	IO_L10N_MRCC_37
	D15	IO_L11P_SRCC_37
	C13	IO_L11N_SRCC_37
	C14	IO_L12P_VRN_37
	H18	IO_L12N_VRP_37
	G17	IO_L13P_37
	M17	IO_L13N_37
	L17	IO_L14P_37
	K18	IO_L14N_VREF_37
	J18	IO_L15P_37
	P17	IO_L15N_37
	N18	IO_L16P_37
	K19	IO_L16N_37
	L19	IO_L17P_37
	R17	IO_L17N_37
	P18	IO_L18P_37
	M18	IO_L18N_37
	M19	IO_L19P_37
		IO_L19N_37

XC6VLX550T-FF1760

Test Header
DO NOT LOAD

J15

1	
2	
3	
4	
5	
6	
7	
8	

HEADER 8

U15-25

	F21	IO_L0P_38
	E20	IO_L0N_38
	J22	IO_L1P_38
	K21	IO_L1N_38
	K20	IO_L2P_38
	J20	IO_L2N_38
	G21	IO_L3P_38
	H21	IO_L3N_38
	G18	IO_L4P_38
	H19	IO_L4N_VREF_38
	F20	IO_L5P_38
	E19	IO_L5N_38
	E17	IO_L6P_38
	F17	IO_L6N_38
	K22	IO_L7P_38
	L22	IO_L7N_38
	L21	IO_L8P_SRCC_38
	L20	IO_L8N_SRCC_38
	D21	IO_L9P_MRCC_38
	C21	IO_L9N_MRCC_38
	D17	IO_L10P_MRCC_38
	D18	IO_L10N_MRCC_38
	B21	IO_L11P_SRCC_38
	A21	IO_L11N_SRCC_38
	C18	IO_L12P_VRN_38
	C19	IO_L12N_VRP_38
	A19	IO_L13P_38
	A20	IO_L13N_38
	B18	IO_L14P_38
	B19	IO_L14N_VREF_38
	F19	IO_L15P_38
	E18	IO_L15N_38
	B16	IO_L16P_38
	B17	IO_L16N_38
	C20	IO_L17P_38
	D20	IO_L17N_38
	A16	IO_L18P_38
	A17	IO_L18N_38
	G19	IO_L19P_38
	E20	IO_L19N_38

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	AH6	IO_L0P_42
	AH5	IO_L0N_42
	AL1	IO_L1P_42
	AK2	IO_L1N_42
	AR2	IO_L2P_42
	AP2	IO_L2N_42
	AL2	IO_L3P_42
	AK3	IO_L3N_42
	AT2	IO_L4P_42
	AT1	IO_L4N_VREF_42
	AJ6	IO_L5P_42
	AJ5	IO_L5N_42
	AL4	IO_L6P_42
	AK4	IO_L6N_42
	AM1	IO_L7P_42
	AM2	IO_L7N_42
	AV1	IO_L8P_SRCC_42
	AU1	IO_L8N_SRCC_42
	AP1	IO_L9P_MRCC_42
	AN1	IO_L9N_MRCC_42
	AL5	IO_L10P_MRCC_42
	AK5	IO_L10N_MRCC_42
	AR3	IO_L11P_SRCC_42
	AN3	IO_L11N_SRCC_42
	AU2	IO_L12P_VRN_42
	AU3	IO_L12N_VRP_42
	AM4	IO_L13P_42
	AM3	IO_L13N_42
	AH10	IO_L14P_42
	AH9	IO_L14N_VREF_42
	AJ8	IO_L15P_42
	AK7	IO_L15N_42
	AR4	IO_L16P_42
	AR3	IO_L16N_42
	AN4	IO_L17P_42
	AN5	IO_L17N_42
	AM6	IO_L18P_42
	AL6	IO_L18N_42
	AH8	IO_L19P_42
	AJ7	IO_L19N_42

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U15-27

	AD6	IO_L0P_43
	AD5	IO_L0N_43
	AG1	IO_L1P_43
	AF1	IO_L1N_43
	AC10	IO_L2P_43
	AC11	IO_L2N_43
	AG5	IO_L3P_43
	AF5	IO_L3N_43
	AF4	IO_L4P_43
	AE4	IO_L4N_VREF_43
	AF6	IO_L5P_43
	AE6	IO_L5N_43
	AG7	IO_L6P_43
	AG6	IO_L6N_43
	AH1	IO_L7P_43
	AJ1	IO_L7N_43
	AF10	IO_L8P_SRCC_43
	AF9	IO_L8N_SRCC_43
	AE7	IO_L9P_MRCC_43
	AD7	IO_L9N_MRCC_43
	AH4	IO_L10P_MRCC_43
	AG4	IO_L10N_MRCC_43
	AD8	IO_L11P_SRCC_43
	AE8	IO_L11N_SRCC_43
	AG9	IO_L12P_VRN_43
	AG8	IO_L12N_VRP_43
	AD10	IO_L13P_43
	AD11	IO_L13N_43
	AH3	IO_L14P_43
	AG3	IO_L14N_VREF_43
	AF7	IO_L15P_43
	AF8	IO_L15N_43
	AJ3	IO_L16P_43
	AJ2	IO_L16N_43
	AE10	IO_L17P_43
	AE9	IO_L17N_43
	AG12	IO_L18P_43
	AG11	IO_L18N_43
	AF12	IO_L19P_43
	AF11	IO_L19N_43

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U15-28

	AA6	IO_L0P_44
	AA7	IO_L0N_44
	W1	IO_L1P_44
	W2	IO_L1N_44
	AB9	IO_L2P_44
	AB8	IO_L2N_44
	Y3	IO_L3P_44
	Y2	IO_L3N_44
	AC6	IO_L4P_44
	AC5	IO_L4N_VREF_44
	AA4	IO_L5P_44
	AA5	IO_L5N_44
	AC1	IO_L6P_44
	AB3	IO_L6N_44
	AA1	IO_L7P_44
	AA2	IO_L7N_44
	AB1	IO_L8P_SRCC_44
	AB2	IO_L8N_SRCC_44
	Y7	IO_L9P_MRCC_44
	Y6	IO_L9N_MRCC_44
	AC4	IO_L10P_MRCC_44
	AB4	IO_L10N_MRCC_44
	Y10	IO_L11P_SRCC_44
	AA11	IO_L11N_SRCC_44
	AC9	IO_L12P_VRN_44
	AC8	IO_L12N_VRP_44
	Y5	IO_L13P_44
	Y4	IO_L13N_44
	AD3	IO_L14P_44
	AD2	IO_L14N_VREF_44
	AB7	IO_L15P_44
	AB6	IO_L15N_44
	AD1	IO_L16P_44
	AC1	IO_L16N_44
	AA10	IO_L17P_44
	AB11	IO_L17N_44
	AE3	IO_L18P_44
	AE2	IO_L18N_44
	AA9	IO_L19P_44
	Y9	IO_L19N_44

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U15-29

	R4	IO_L0P_45
	R3	IO_L0N_45
	N1	IO_L1P_45
	M1	IO_L1N_45
	V9	IO_L2P_45
	V10	IO_L2N_45
	P2	IO_L3P_45
	P3	IO_L3N_45
	V4	IO_L4P_45
	U4	IO_L4N_VREF_45
	U6	IO_L5P_45
	T5	IO_L5N_45
	W3	IO_L6P_45
	V3	IO_L6N_45
	U11	IO_L7P_45
	V11	IO_L7N_45
	W9	IO_L8P_SRCC_45
	W7	IO_L8N_SRCC_45
	T6	IO_L9P_MRCC_45
	T7	IO_L9N_MRCC_45
	V5	IO_L10P_MRCC_45
	V6	IO_L10N_MRCC_45
	R5	IO_L11P_SRCC_45
	T4	IO_L11N_SRCC_45
	W11	IO_L12P_VRN_45
	W10	IO_L12N_VRP_45
	U8	IO_L13P_45
	U9	IO_L13N_45
	U2	IO_L14P_45
	U3	IO_L14N_VREF_45
	W6	IO_L15P_45
	W5	IO_L15N_45
	T1	IO_L16P_45
	T2	IO_L16N_45
	P1	IO_L17P_45
	R2	IO_L17N_45
	V1	IO_L18P_45
	U1	IO_L18N_45
	V8	IO_L19P_45
	U7	IO_L19N_45

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U15-30

	L5	IO_L0P_46
	M6	IO_L0N_46
	D1	IO_L1P_46
	E2	IO_L1N_46
	T9	IO_L2P_46
	R10	IO_L2N_46
	F1	IO_L3P_46
	F2	IO_L3N_46
	L4	IO_L4P_46
	M4	IO_L4N_VREF_46
	N5	IO_L5P_46
	N6	IO_L5N_46
	L2	IO_L6P_46
	K2	IO_L6N_46
	G3	IO_L7P_46
	H3	IO_L7N_46
	G1	IO_L8P_SRCC_46
	G2	IO_L8N_SRCC_46
	K3	IO_L9P_MRCC_46
	K4	IO_L9N_MRCC_46
	P5	IO_L10P_MRCC_46
	N4	IO_L10N_MRCC_46
	N8	IO_L11P_SRCC_46
	P7	IO_L11N_SRCC_46
	T11	IO_L12P_VRN_46
	T10	IO_L12N_VRP_46
	R9	IO_L13P_46
	P10	IO_L13N_46
	N3	IO_L14P_46
	M3	IO_L14N_VREF_46
	R7	IO_L15P_46
	P6	IO_L15N_46
	J1	IO_L16P_46
	H1	IO_L16N_46
	P8	IO_L17P_46
	R8	IO_L17N_46
	L1	IO_L18P_46
	M2	IO_L18N_46
	J2	IO_L19P_46
	J3	IO_L19N_46

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	J6	IO_L0P_47
	K7	IO_L0N_47
	B4	IO_L1P_47
	A4	IO_L1N_47
	H5	IO_L2P_47
	H6	IO_L2N_47
	B3	IO_L3P_47
	A2	IO_L3N_47
	N9	IO_L4P_47
	N10	IO_L4N_VREF_47
	F5	IO_L5P_47
	E5	IO_L5N_47
	H4	IO_L6P_47
	G4	IO_L6N_47
	C5	IO_L7P_47
	D5	IO_L7N_47
	M8	IO_L8P_SRCC_47
	L9	IO_L8N_SRCC_47
	C3	IO_L9P_MRCC_47
	C4	IO_L9N_MRCC_47
	F4	IO_L10P_MRCC_47
	E4	IO_L10N_MRCC_47
	N13	IO_L11P_SRCC_47
	P13	IO_L11N_SRCC_47
	L6	IO_L12P_VRN_47
	M7	IO_L12N_VRP_47
	M5	IO_L13P_47
	L5	IO_L13N_47
	E3	IO_L14P_47
	D3	IO_L14N_VREF_47
	K5	IO_L15P_47
	J5	IO_L15N_47
	B1	IO_L16P_47
	B2	IO_L16N_47
	M11	IO_L17P_47
	N11	IO_L17N_47
	C1	IO_L18P_47
	D2	IO_L18N_47
	P11	IO_L19P_47
	P12	IO_L19N_47

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