

last update 080710

BNL TASK LIST

1. Test system deadtime – use max tokens in early April
2. TACS – need QT decision first
 - a. fix broken board in BNL Office
 - b. prepare TAC input for PECL if we upgrade to QTs for BBC,ZDC
3. L1
 - a. Design/fab/test new TCU – MN, EJ
 - b. Re-distribute boards in L1 crate to accommodate double-wide TCU - JE
 - c. Re-cable crate - JE
4. FMS
 - a. Re-test QT boards before beginning of run 9 – HC,CP,JE,MN
 - b. test new cluster triggers – MN,EJ,HC
 - c. rebuild LED control unit to get all in 1 crossing - DP
 - d. repair 0x1f DSM – JE
 - e. recable for EJ trigger patch plan – LB, AG, AO
5. BEMC
 - a. rewrite VHDL for new cabling scheme – EJ
 - b. recable for new scheme - JE
6. MTD
 - a. Troubleshoot communication problem to V895s (Cayenne discs) - JE
7. TCD
 - a. Arrange to have spares repaired - DP
 - b. Troubleshoot “Group 2” problem – GV, JL – may be fixed already
8. BBC – need QT decision
 - a. Troubleshoot and repair large tile CDB problem in BDB crate
 - b. check disc thr. on small disks – LB, AO
 - c. replace low-gain PMTs – LB, AO
 - d. convert to QT - JE
9. ZDC
 - a. convert to QT - JE
10. Scaler
 - a. Fix RAT board Ethernet broadcast problem - CP
 - b. cable and test 48bit scaler – CP,HC
11. TOF
 - a. design, fab and test proto-type TOF-DSMI, fab 10 units - DP
 - b. trim VPD cables or find 80ns gate CDBs – JE
12. STP
 - a. Fix PMC 1st few event pollution – CP
 - b. add L2->L1 path to eliminate myrinet - CP
13. FPDE
 - a. convert to QT - JE
14. FHC – awaiting proposal (HC,LB,MN)
 - a. install new forward hadron calorimeter
 - b. design FHC -> trigger interface

15. Platform (general)
 - a. Get broken fibers repaired (4 or 5 known) - JE
 - b. diagnose rate-dependent DMA errors in BC1 – JN, JE
 - c. fix missing spin bits – JE, ES
 - d. diagnose GLINK “crosstalk” – HC says this is result of using ECL discriminator fed by TTL signal
 - e. Test all DSMs before run 9 - JE, DP
 - f. Test all CDBs and TACs before run 9 – JE, DP
 - g. Get myrinet monitors hooked up to Ethernet and configure - JE
16. L2 work
 - a. Build new quad-CPU L2 machine (requires 2 pci {stp} and >4 PCI-X {myrinet, TOF DRORC, BTOW DRORC, ETOW DRORC}) – JE, CP, JN
 - b. Implement additional DRORC card into L2 for TOF – pending physics requirement for justification – JN (TOF has 2 DRORCs ordered 080710)
 - c. Get ORDO working from L2ana02 – JE 080330
 - d. Design and implement administrative backup system for L2 machines (l2ana01, l2ana02, trgscratch) and get all code in CVS - JN
 - e. Fix NTP server on L2 machines to repair time-synch problem – JE 080330
 - f. fix EST<->EDT problem – JE 080330
 - g. test PCI -> PCIE card – CP (hdwr arrives 8 Aug)
 - h. upgrade all code for new trgStructures.h - JN
17. Counting house (general)
 - a. Move all startrg functionality to startrg2 - 080630
 - b. Remove old sun machine (startrg) - 080630
 - c. Replace startrg – with MAC? (vxworks cross compilers are all on daqman) or perhaps old L2ANA01 - JE
 - d. Setup UPS communication with startrg2 - JE
18. General
 - a. Configure machines in testlab to work on new network - 080630
 - b. repair and test DSMs, CDBs, DIBs,
 - c. test all equipment before run 9
 - d. remove all unused tier1 files to archive - 080530
19. documentation
 - a. map out data movement (e.g. scaler -> l2ana02 -> HPSS/trgscratch, STP->l2an01 -> trgscratch -> machank) - JN
 - b. test gzip on trgscratch prior to moving data - JN
 - c. describe EMC<->TRG connections on web – JN, JE
 - d. post fiber document – JE
 - i. get overall star fiber map from dan
 - ii. finish fiber owners and users document
 - iii. create web table with info
 - e. inventory DSMs and CDBs on wiki page - 080615
20. diagnose JP trigger inefficiencies - JB
21. test trigger to pp2pp connection - JE

22. implement GbE connection between trigger and daq - JE