

Last update: 090209: HjC

TCU Input Bits – Old TCU for Run9

Bit	Ped	Tune	Physics
	Tier1 = Ped_090313	Tune_090313	Trg_090313
0		ZDC_Back_East_ADC_GE_th	BEMC_bits_and_BBCMB
1		ZDC_Front_West_ADC_GE_th	EEMC_bits_and_BxEEMC_JP
2		ZDC_Back_West-ADC_GE_th	BEMC_HT_bit0
3		VPD_TAC_diff_n_window	BEMC_HT_bit1
4		BBCsmall_East_ADC_sum_GE_th	VPDMB_no_prescale_no_live
5		BBCsmall_TAC_diff_n_window	EHT2
6		BBCsmall_West_ADC_sum_GE_th	JP1
7		ZDC_TAC_diff_n_window	BBCMBLive_prescaled
8		ZDC_East_ADC_GE_th	VPDMBLive_prescaled
9		ZDC_West ADC_GE_th	ZDCMB_prescaled
10		ZDC_Front_East_ADC_GE_th	ZDCpol
11		VPD_East_ADC_sum_GE_th	MTD
12		VPD_West_ADC_sum_GE_th	FMSfast
13	Minimum bias	BEMC_HT_0_or_EEMC_HT_0	FMSslow
14	zerobias	FMS_cluster_GE_th0	FMSled-FPE
15	random	ZeroBias	ZeroBias

To get bit definitions, just cat the tier1 file (~trg/cfg/Tier1) and then more the TCU_bit_def file