

Last Update: 110126

Scaler Fan-Out Board

input bit	name	source
1	experiment clock	RCC/1
2	FMS_LED	LEMO 0
3	TCUIN 9	TCUIN 9
4	RUN/STOP	RCCIN 3
5	REVTICK	REVTICKIN 0
6		
7		
8		

bits are numbered from 1, not 0, since they derive from bins on connectors.