

INPUTS				LEMO (TTL) INPUTS				OUTPUTS			
1		this is used as	1		1		0	FMS LED	J48	BX202	
2		LEMO input	2		2		1	EPD_CR trigger	J45	GLINK 3T	
3		based on Jumpers	3		3		2		J44		
4	J		4	J	4	J	3		J43		
5	1		5	3	5	7	4		J68		
6	5		6	0	6	1	5		J67		
7			7		7		6		J64	Scaler4-Hi	
8			8	from BBCW	8	from BBCE	7	TOF	J65	Scaler4-Lo	
9			9		9		8		J33	Scaler5-Hi	
10			10		10		9		J34	Scaler5-Lo	
11			11		11		10		J72	Scaler3-Lo	
12			12		12		11		J73	Scaler6-Hi	
13			13		13		12		J35	Scaler6-Lo	
14			14		14		13		J36	TCU	
15			15		15		14		J47	BX201	
16			16		16		15		J41	Scaler2-Lo	
									J42	Scaler3-Hi	
									J37	FANOUT	
									J38	Scaler1-Hi	
									J39	Scaler1-Lo	
									J40	Scaler2-Hi	
1	J	BBCW L to FO/6	1	J	1	J					
2		BBCW L to FO/7	2		2						
3			3		3						
4			4	J	4	J					
5	6		5	5	5	4					
6	6		6	8	6	9					
7		from BB102	7		7						
8			8	from TCUI	8	from VT201	1	GLINK4R inputs	J15	INPUTS - DICT	
9			9	TCU 0:15	9		2	Y-filled	J30	ITLIN	
10			10	PHYSbits	10		3		J49	VT201IN	
11			11		11		4		J50	QTSMDIN	
12			12		12		5		J51	BX202IN	
13			13		13		6	B-filled	J52	ZD101IN	
14			14		14		7		J53	VPDEIN	
15			15		15		8		J54	TF201IN	
16			16		16		9		J55	PP2PPIN	
							10		J56	REV1ICKIN	
							11		J57	VPDWIN	
							12		J58	TCUIN	
							13		J59	TCURIN	
							14		J60	GLINKIN (GLINK4)	
							15		J61	RCCIN	
							16		J66	BB102IN	
1	J		1	J	1	J					
2			2		2						
3			3		3						
4			4	J	4	J					
5	7		5	5	5	5					
6	0		6	4	6	0					
7		from BB101	7		7		0	J56 Rev1ickIn	J71	BBCEIN	
8			8	from TF201	8	from QTSMD	1	rev1ick	J75	HEADERIN	
9			9		9		2	TCIM PECL ZDCE			
10			10		10		3	TCIM PECL ZDCW			
11			11		11		4	TCIM PECL ZDCE.W			
12			12		12		5	RHIC1			
13			13		13		6	RHIC2			
14			14		14		7	RHIC3			
15			15		15		8	RHIC4			
16			16		16		9				
							10				
							11				
							12				
							13				
							14				
							15				
1	J		1	J	1	J					
2			2		2						
3			3		3						
4			4	J	4	J					
5	6		5	5	5	5					
6	9		6	2	6	3					
7		from VP101	7		7						
8			8	from ZD101	8	from VPDW TAC	0	TCUout			
9			9		9		1	FMS LED			
10			10		10		2	ZDCE.W			
11			11		11		3	REV1IC			
12			12		12		4	RHIC1			
13			13		13		5	about gap			
14			14		14		6	Yfilled			
15			15		15		7	YUP			
16			16		16		8	YDN			
							9	YUN			
							10	BUN			
							11	BUP			
							12	EPD CR Trigger			
							13	Det2 live			
							14	Det3 live			
							15	Det4 live			
								Det5 live			
1	J	clock	1	J	1	J					
2		latch	2		2						
3		run/stop	3		3						
4	J	halt	4	J	4	J					
5	6		5	5	5	5					
6	1		6	1	6	7					
7			7		7						
8		from RCC	8	from BX202	8	from VPDW TAC	0	GLINK 3T out			
9			9		9		1	RCC			
10			10		10		2	PP2PP			
							3	PP2PP			
							4	PP2PP			
							5	PP2PP			
							6	PP2PP			
							7	PP2PP			
							8	PP2PP			
							9	PP2PP			
							10	PP2PP			
							11	PP2PP			
							12	PP2PP			
							13	PP2PP			
							14	PP2PP			
							15	PP2PP			
1	J		1	J	1	J					
2			2		2						
3			3		3						
4			4	J	4	J					
5	5		5	5	5	5					
6	9		6	5	6	6					
7		from TCUR	7		7						
8		DetLIVE 0-15	8	from PP2PPTAC	8	from REV TIC					
9		16=contam	9		9						
10		17-20 unused	10		10						
11			11		11						
12			12		12						
13			13		13						
14			14		14						
15			15		15						
16			16		16						