

		INPUTS				LEMO (TTL) INPUTS	OUTPUTS
1		this is used as	1		1	0	J48 BX202
2		LEMO input	2		2	1	J45 GLINK 3T
3		based on Jumpers	3		3	2	J44
4	J		4	J	4	3	J43
5	1		5	3	5	4	J68
6	5		6	0	6	5	J67
7			7		7	6	J64
8			8	from BBCW	8	7	J65 Scaler4-Hi
9			9		9	8	J33 Scaler5-Hi
10			10		10	9	J34 Scaler5-Lo
11			11		11	10	J72 Scaler3-Lo
12			12		12	11	J73 Scaler6-Hi
13			13		13	12	J35 Scaler6-Lo
14			14		14	13	J36 TCU
15			15		15	14	J47 BX201
16			16		16	15	J41 Scaler2-Lo
							J42 Scaler3-Hi
							J37 FANOUT
							J38 Scaler1-Hi
							J39 Scaler1-Lo
							J40 Scaler2-Hi
1			1		1		
2		BBCW L to FO/6	2		2		
3		BBCW L to FO/7	3		3		
4	J		4	J	4		
5	6		5	5	5		
6	6		6	8	6	4	J
7		from BB102	7		7	9	
8			8	from TCUI	8	1	GLINK4R inputs
9			9	TCU 0:15	9	2	Y-filled
10			10	PHYSbits	10	3	B-filled
11			11		11	4	J15 INTPUS - DICT
12			12		12	5	J30 TTLIN
13			13		13	6	J49 VT201IN
14			14		14	7	J50 QTSMDIN
15			15		15	8	J51 BX202IN
16			16		16	9	J52 ZDI01IN
						10	J53 VPDWIN
						11	J54 TF201IN
						12	J55 PP2PPIN
						13	J56 REV1ICKIN
						14	J57 VPDWIN
						15	J58 TCUIIN
						16	J59 TCORIN
1			1		1		
2			2		2		
3	J		3	J	3		
4	7		4	5	4		
5	0		5	4	5	5	J
6		from BB101	6		6	0	
7			7	from TF201	7		J56 Rev1ickIn
8			8		8	1	rev1ick
9			9	from QTSMD	9	2	TCIM PECL ZDCE
10			10		10	3	TCIM PECL ZDCW
11			11		11	4	TCIM PECL ZDCE.W
12			12		12	5	
13			13		13	6	
14			14		14	7	
15			15		15	8	
16			16		16	9	
						10	
						11	
						12	
						13	
1			1		1		
2			2		2		
3	J		3	J	3		
4	6		4	5	4		
5	9		5	2	5	5	J
6		from VP101	6		6	3	
7			7	from ZD101	7		TCUout
8			8		8	0	rev1ick-1
9			9		9	1	rev1ick-2
10			10		10	2	rev1ick-3
11			11		11	3	rev1ick-4
12			12		12	4	GLINK41-Y filed
13			13		13	5	GLINK41-1
14			14		14	6	GLINK41-2: B filed
15			15		15	7	GLINK41-3
16			16		16	8	GLINK41-4
						9	GLINK41-5
						10	TTLIN-0
						11	TTLIN-1: EPD CR Trigger
						12	TTLIN-2
						13	TTLIN-3
						14	TTLIN-4
						15	TTLIN-5
1			1		1		
2		clock	2		2		
3	J	latch	3	J	3		
4	6	run/stop	4		4		
5	1	halt	5	1	5		
6			6		6		
7			7		7		
8			8	from BX202	8		
9			9		9		
10			10		10		
			11		11		
			12		12		
			13		13		
			14		14		
			15		15		
			16		16		
1			1		1		
2			2		2		
3	J		3	J	3		
4	5		4	5	4		
5	9		5	5	5		
6		from TCUR	6		6		
7		DetLIVE 0-15	7		7		
8		16=contam	8		8		
9		17-20 unused	9		9		
10			10		10		
11			11		11		
12			12		12		
13			13		13		
14			14		14		
15			15		15		
16			16		16		